

PATENT APPLICATION SERIAL NO. _____

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE
FEE RECORD SHEET

05/21/2004 HLE333 00000024 090458 10709652

01 FC:1001 770.00 DA

PTO-1556
(5/87)

Electronic Filing System (EFS) Data

Electronic Patent Application Submission

USPTO Use Only

EFS ID: 61275
Application ID: 10709652
Title of Invention: REDUCED DIELECTRIC
CONSTANT SPACER MATERIALS
INTEGRATION FOR HIGH SPEED
LOGIC GATES
First Named Inventor: Michael Belyansky
Domestic/Foreign Application: Domestic Application
Filing Date: 2004-05-20
Effective Receipt Date: 2004-05-20
Submission Type: Utility Patent Filing
Filing Type: new-utility
Confirmation number: 3651
Attorney Docket Number: FIS920040047US1



Total Fees Authorized: 810.0
Payment Category: Deposit Account
Deposit Account Number: 90458
Deposit Account Name: H. Daniel Schnurmann
RAM Payment Status: RAM has not been processed

Digital Certificate Holder: cn=H. Daniel Schnurmann,ou=Registered Attorneys,ou=Patent and
Trademark Office,ou=Department of Commerce,o=U.S. Government,c=US
Certificate Message Digest: 30562544e4207dc7516e8fdb44ec2b3a3c51ea6e